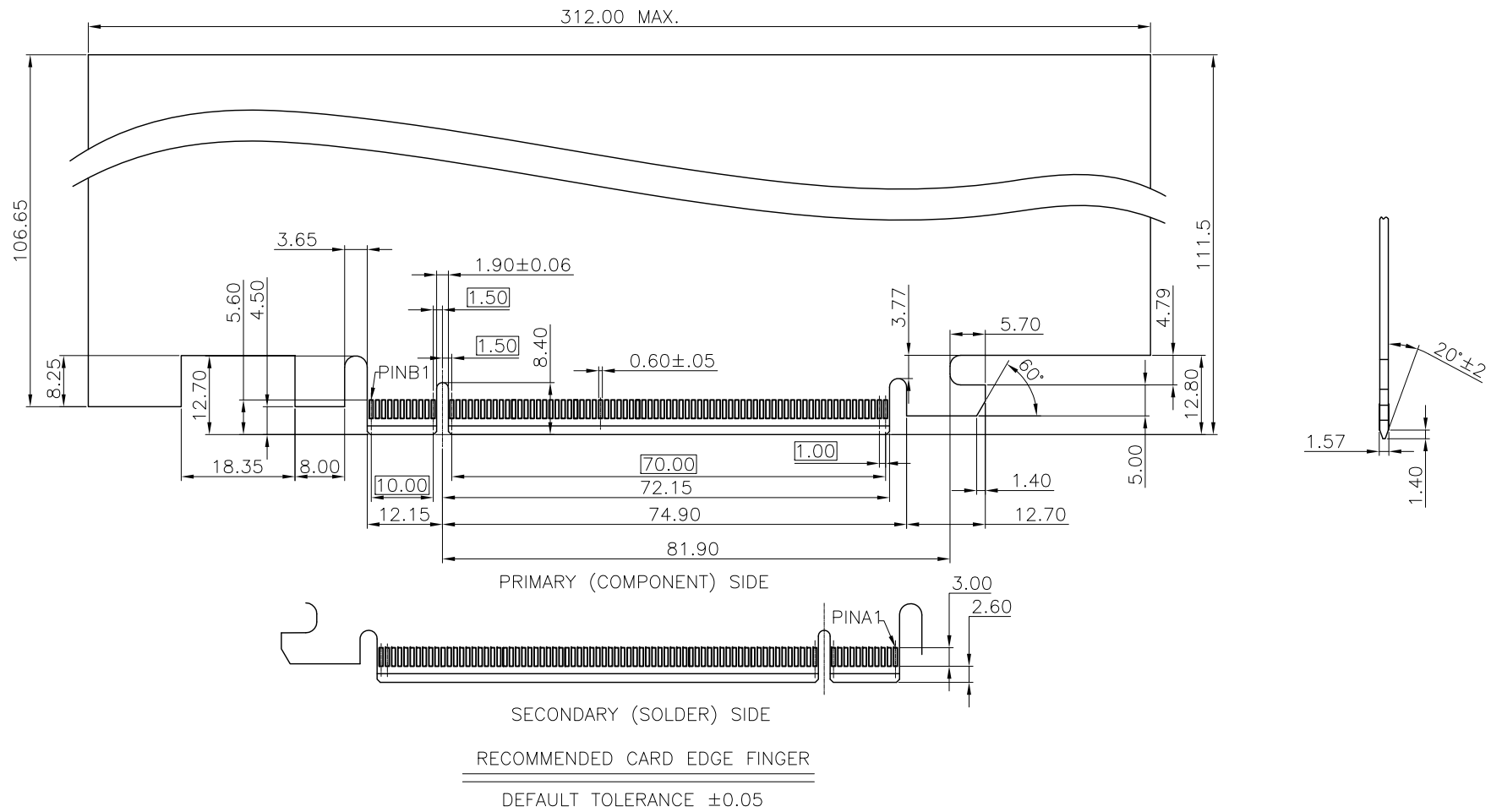


RoHS GP HF

REV.	ECN MARK	ECN NO.	DESCRIPTION	DRAWN	DATE
A	⊗	/	NEW	ZHUCHAO	2022/08/31



NOTES:

- NO TIE PERMITTED FROM CARD EDGE TO LEADING EDGE OF PAD FOR PINS 'A' AND 'C'.
- CHAMFER EDGES MUST BE FREE OF CUTTING BURRS.

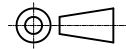
TOLERANCE X.X ±0.35 X.XX ±0.25 X.XXX ±0.10 ANGLE ±2.0°	MODEL NAME:	PCIE 164PIN,GEN5 SMT			DRAWN:	ZHUCHAO
	MODEL NO.:	SEE TABLE			CHECKED:	YOUHUA.CAI
	UNIT:	SCALE:	REVISION:	SHEET:	APPROVED:	YOUHUA.CAI
	mm	1:1	A	2/2	DATE:	2022/08/31
	WLCO 深圳市兴万联电子有限公司 SHENZHEN XING WAN LIAN ELECTRONICS CO.,LTD.				DWG NO.:	C-WLPC-164P35
					FILE:	C

1 2
RoHS **GP** **HF**

REV.	ECN MARK	ECN NO.	DESCRIPTION	DRAWN	DATE
A	⊗	/	NEW	ZHUCHAO	2022/08/31

TABLE

NO.	PART NO.	SPECIFICATION	HSF COMPLIANT
1	WLPC-147A	HOUSING_PA10T BLACK UL94-V0,CONTACT_C5191 Au15u” Sn100u”,HOOK_S50C Sn100u”,GROUND SUS301.	HF COMPLIANT

TOLERANCE X.X ±0.35 X.XX ±0.25 X.XXX ±0.10 ANGLE ±2.0°	MODEL NAME:	PCIE 164PIN,GEN5 SMT			DRAWN:	ZHUCHAO
	MODEL NO:	SEE TABLE			CHECKED:	YOUHUA.CAI
	UNIT:	SCALE:	REVISION:	SHEET:	APPROVED:	YOUHUA.CAI
	mm	1:1	A	1/1	DATE:	2022/08/31
	WLCO 深圳市兴万联电子有限公司 SHENZHEN XING WAN LIAN ELECTRONICS CO.,LTD.				DWG NO:	C-WLPC-147A
					FILE:	C